Dr. Milenko Milicevic

System Architect and Senior Analog/RF IC Design Engineer

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linkedin

Core focus: RFIC, MMIC, analog mixed-signal IC design, EM simulation, radar and high-frequency systems up to 120 GHz

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Professional Profile

Experienced System Architect and Senior Analog/RF IC Design Engineer with more than 13 years of industry experience in RF, MMIC and analog mixed-signal integrated circuits. Strong background in end-to-end development from system definition and feasibility studies to circuit design, layout, verification and silicon validation. Proven experience in automotive and high-frequency applications, including FMCW/CW radar and electromagnetic modeling up to 120 GHz.

Professional Experience

System Architect June 2024 - August 2026
TES Electronic Solutions GmbH, Stuttgart, Germany

- Define RF system concepts and support architecture decisions for IC development programs.
- Perform link budget analysis and evaluate suitable technologies for new product requirements.
- Coordinate development of multiple IC building blocks across project phases.
- Contribute to feasibility studies and technical quotations for customer projects.

Senior Analog/RF IC Design Engineer September 2018 - August 2026
TES Electronic Solutions GmbH, Stuttgart Area, Germany

- Designed RFIC, MMIC and analog mixed-signal IC blocks for high-frequency and automotive applications.
- Selected topologies for key building blocks and supported implementation from schematic to layout.
- Performed electromagnetic simulation and modeling for circuits operating up to 120 GHz.
- Supported system-level design, verification, lab validation and customer technical assessments.

Analog/RF IC Design Engineer June 2012 - September 2018
TES Electronic Solutions GmbH

- Designed and laid out RFIC, MMIC and analog mixed-signal integrated circuits.
- Carried out analog mixed-signal verification and silicon lab validation activities.
- Performed feasibility studies, electromagnetic simulations and RF system-level analyses.

Selected Publications / Technical Topics

- Temperature and process compensated RF power detector
- Temperature and process compensated broadband CMOS RF power detector
- Digitally controlled attenuator
- Low-pass filter for UWB systems with compensation of process-induced on-chip capacitor variation
- Power and conjugately matched high-band UWB power amplifier

Core Competencies

- RF and MMIC integrated circuits
- Analog mixed-signal IC design
- RF system design
- FMCW/CW radar
- High-voltage design
- Automotive electronics
- Electromagnetic simulation and modeling
- IC layout and verification
- Silicon lab validation

EDA Tools

- Cadence Virtuoso
- EMX
- Calibre
- ADS
- Momentum

Process Experience

- CMOS from 180 nm down to 28 nm
- BCD
- SOI
- SiGe
- BiCMOS

Education

PhD, Electronics
University of Belgrade
2012 - 2019

Master's Degree, Electronics
University of Belgrade
2010 - 2012

Bachelor's Degree, Electrical and Electronics Engineering
University of Belgrade
2006 - 2010

Languages

- English: Full professional proficiency
- German: Limited working proficiency